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| 4001 | 4 | Quad 2-input NOR gate |
| 4002 | 2 | Dual 4-input NOR gate |
| 4006 | 1 | 18-stage shift register |
| 4007 | 2 | Dual complementary transistor pair + 1 NOT gate |
| 4008 | 1 | 4-bit binary full adder |
| 40098 | 6 | Hex 3-state inverting buffer |
| 4009 | 6 | Hex inverting buffer (replaced by 4049) |
| 40100 | 1 | 32-bit left/right shift register |
| 40101 | 1 | 9-bit parity generator/checker |
| 40102 | 1 | Presettable 2-decade BCD down counter |
| 40103 | 1 | Presettable 8-bit binary down counter |
| 40104 | 1 | 4-bit bidirectional parallel-in/parallel-out shift register (tri-state) |
| 40105 | 1 | 4-bit x 16 word FIFO register |
| 40106 | 6 | Hex inverting Schmitt trigger-(NOT gates) |
| 40107 | 2 | Dual 2-input NAND buffer/driver |
| 40108 | 1 | 4x4-bit (tri-state) synchronous triple-port register file |
| 40109 | 4 | Quad level shifter |
| 4010 | 6 | Hex non-inverting buffer (replaced by 4050) |
| 40110 | 1 | Up/down counter latch decoder driver |
| 40116 | 1 | 8-bit bidirectional CMOS-to-TTL level converter |
| 40117 | 1 | Programmable dual 4-bit terminator |
| 4011 | 4 | Quad 2-input NAND gate |
| 4012 | 2 | Dual 4-input NAND gate |
| 4013 | 2 | Dual D-type flip-flop |
| 40147 | 1 | 10-line to 4-line (BCD) priority encoder |
| 4014 | 1 | 8-stage shift register |
| 4015 | 2 | Dual 4-stage shift register |
| 40160 | 1 | Decade counter/asynchronous clear |
| 40161 | 1 | Binary counter/asynchronous clear |
| 40162 | 1 | 4-bit synchronous decade counter with load, reset, and ripple carry output |
| 40163 | 1 | 4-bit synchronous binary counter with load, reset, and ripple carry output |
| 4016 | 4 | Quad bilateral switch |
| 40174 | 6 | Hex D-type flip-flop |
| 40175 | 4 | Quad D-type flip-flop |

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| 4017 | 1 | Decade counter with 10 decoded outputs (5-stage Johnson counter) |
| 40181 | 1 | 4-bit 16-function arithmetic logic unit |
| 4018 | 1 | Presettable divide-by-N counter |
| 40192 | 1 | Presettable 4-bit up/down BCD counter |
| 40193 | 1 | Presettable 4-bit up/down binary counter |
| 40194 | 1 | 4-bit bidirectional universal shift register |
| 40195 | 1 | 4-bit universal shift register |
| 4019 | 4 | Quad AND/OR select gate |
| 40208 | 1 | 4 x 4-bit (tri-state) synchronous triple-port register file |
| 4020 | 1 | 14-stage binary ripple counter |
| 4021 | 1 | 8-stage shift register |
| 4022 | 1 | Octal counter with 8 decoded outputs (4-stage Johnson counter) |
| 4023 | 3 | Triple 3-input NAND gate |
| 40240 | 1 | Buffer/Line driver; inverting (tri-state) |
| 40244 | 1 | Buffer/line driver; non-inverting (tri-state) |
| 40245 | 1 | Octal bus transceiver; (tri-state) outputs |
| 4024 | 1 | 7-stage binary ripple counter |
| 4025 | 3 | Triple 3-input NOR gate |
| 40257 | 4 | Quad 2-line to 1-line data selector/multiplexer (tri-state) |
| 4026 | 1 | Decade counter with decoded 7-segment display outputs and display enable |
| 4027 | 2 | Dual J-K master-slave flip-flop |
| 4028 | 1 | BCD to decimal (1-of-10) decoder |
| 4029 | 1 | Presettable up/down counter, binary or BCD-decade |
| 4030 | 4 | Quad XOR gate (replaced by 4070) |
| 4031 | 1 | 64-stage shift register |
| 4032 | 3 | Triple serial adder |
| 4033 | 1 | Decade counter with decoded 7-segment display outputs and ripple blanking |
| 4034 | 1 | 8-stage bidirectional parallel/serial input/output register |
| 4035 | 1 | 4-stage parallel-in/parallel-out shift register |
| 40373 | 1 | Octal D-type transparent latch (tri-state) |
| 40374 | 1 | Octal D-type flip-flop; positive-edge trigger (tri-state) |
| 4038 | 3 | Triple serial adder |
| 4040 | 1 | 12-stage binary ripple counter |
| 4041 | 4 | Quad true/complement buffer |
| 4042 | 4 | Quad D-type latch |
| 4043 | 4 | Quad NOR R/S latch with tri-state outputs |

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| 4044 | 4 | Quad NAND R/S latch with tri-state outputs |
| 4045 | 1 | 21-stage counter |
| 4046 | 1 | Phase-locked loop with VCO |
| 4047 | 1 | Monostable/astable multivibrator |
| 4048 | 1 | Multifunctional expandable 8-input gate with tri-state output |
| 4049 | 6 | Hex buffer/converter (inverting) |
| 4050 | 6 | Hex buffer/converter (non-inverting) |
| 4051 | 1 | 8-channel analog multiplexer/demultiplexer |
| 4052 | 2 | Dual 4-channel analog multiplexer/demultiplexer |
| 4053 | 3 | Triple 2-channel analog multiplexer/demultiplexer |
| 4054 | 1 | 4-segment LCD driver |
| 4055 | 1 | BCD to 7-segment decoder/LCD driver with "display-frequency" output |
| 4056 | 1 | BCD to 7-segment decoder/LCD driver with strobed-latch function |
| 4059 | 1 | Programmable divide-by-N counter |
| 4060 | 1 | 14-stage binary ripple counter and oscillator |
| 4062 | ? | Logic dual 3 majority gate |
| 4063 | 1 | 4-bit digital comparator |
| 4066 | 4 | Quad analog switch (low "ON" resistance) |
| 4067 | 1 | 16-channel analog multiplexer/demultiplexer (1-of-16 switch) |
| 4068 | 1 | 8-input NAND gate |
| 4069 | 6 | Hex inverter |
| 4070 | 4 | Quad 2-input XOR gate |
| 4071 | 4 | Quad 2-input OR gate |
| 4072 | 2 | Dual 4-input OR gate |
| 4073 | 3 | Triple 3-input AND gate |
| 4075 | 3 | Triple 3-input OR gate |
| 4076 | 4 | Quad D-type register with tri-state outputs |
| 4077 | 4 | Quad 2-input XNOR gate |
| 4078 | 1 | 8-input NOR/OR gate |
| 4081 | 4 | Quad 2-input AND gate |
| 4082 | 2 | Dual 4-input AND gate |
| 4085 | 2 | Dual 2-wide, 2-input AND/OR invert (AOI) |
| 4086 | ? | Expandable 4-wide, 2-input AND/OR invert (AOI) |
| 4089 | 1 | Binary rate multiplier |
| 4093 | 4 | Quad 2-input Schmitt trigger NAND gate |
| 4094 | 1 | 8-stage shift-and-store bus |

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| 4095 | 1 | Gated J-K flip-flop (non-inverting) |
| 4096 | 1 | Gated J-K flip-flop (inverting and non-inverting) |
| 4097 | 1 | Differential 8-channel analog multiplexer/demultiplexer |
| 4098 | 2 | Dual one-shot monostable |
| 4099 | 1 | 8-bit addressable latch |
| 4104 | 4 | Quad low-to-high voltage translator with tri-state outputs |
| 4106 | 6 | Hex Schmitt trigger |
| 4160 | 1 | Decade counter with asynchronous clear |
| 4161 | 1 | 4-bit binary counter with asynchronous clear |
| 4162 | 1 | Decade counter with synchronous clear |
| 4163 | 1 | 4-bit binary counter with synchronous clear |
| 4174 | 6 | Hex D-type Flip-Flop |
| 4175 | 4 | Quad D-type flip-flop |
| 4192 | 1 | Presettable up-down counter |
| 4490 | 6 | Hex contact bounce eliminator |
| 4500 | 1 | Industrial control unit |
| 4502 | 6 | Hex inverting buffer (tri-state) |
| 4503 | 6 | Hex non-inverting buffer with tri-state outputs |
| 4504 | 6 | Hex voltage level shifter for TTL-to-CMOS or CMOS-to-CMOS operation |
| 4505 | 1 | 64-bit, 1-bit per word random access memory (RAM) |
| 4508 | 2 | Dual 4-bit latch with tri-state outputs |
| 45106 | 1 | frequency synthesizer |
| 4510 | 1 | Presettable 4-bit BCD up/down counter |
| 4511 | 1 | BCD to 7-segment latch/decoder/driver |
| 4512 | 1 | 8-input multiplexer (data selector) with tri-state output |
| 4513 | 1 | BCD to 7-segment latch/decoder/driver (4511 plus ripple blanking) |
| 4514 | 1 | 1-of-16 decoder/demultiplexer active HIGH output |
| 4515 | 1 | 1-of-16 decoder/demultiplexer active LOW output |
| 4516 | 1 | Presettable 4-bit binary up/down counter |
| 4517 | 2 | Dual 64-stage shift register |
| 4518 | 2 | Dual BCD up counter |
| 4519 | 4 | Quad 2-input multiplexer (data selector) |
| 4520 | 2 | Dual 4-bit binary up counter |
| 4521 | 1 | 24-stage frequency divider |
| 4522 | 1 | Programmable BCD divide-by-N counter |
| 4526 | 1 | Programmable 4-bit binary down counter |

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| 4527 | 1 | BCD rate multiplier |
| 4528 | 2 | Dual retriggerable monostable multivibrator with reset |
| 4529 | 2 | Dual 4-channel analog data selector/multiplexer |
| 4530 | 2 | Dual 5-input majority logical gate |
| 4531 | 1 | 12-bit parity tree |
| 4532 | 1 | 8-bit priority encoder |
| 4536 | 1 | Programmable timer |
| 4538 | 2 | Dual retriggerable precision monostable multivibrator |
| 4539 | 2 | Dual 4-input multiplexer |
| 4541 | 1 | Programmable timer |
| 4543 | 1 | BCD to 7-segment latch/decoder/driver with phase input |
| 4549 | 1 | Successive approximation registers |
| 4551 | 4 | Quad 2-channel analog multiplexer/demultiplexer |
| 4553 | 1 | 3-digit BCD counter |
| 4555 | 2 | Dual 1-of-4 decoder/demultiplexer active HIGH output |
| 4556 | 2 | Dual 1-of-4 decoder/demultiplexer active LOW output |
| 4557 | 1 | 1-to-64 bit variable length shift register |
| 4558 | 1 | BCD to 7-segment decoder (enable, RBI and provides active–high output) |
| 4559 | 1 | Successive approximation registers |
| 4560 | 1 | NBCD adder |
| 4562 | 1 | 128–bit static shift register |
| 4566 | 1 | Industrial time-base generator |
| 4569 | 1 | Programmable divide-By-N, dual 4-Bit binary/BCD down counter |
| 4572 | 6 | Hex gate: quad NOT, single NAND, single NOR |
| 4583 | 2 | Dual Schmitt trigger |
| 4584 | 6 | Hex inverting Schmitt trigger |
| 4585 | 1 | 4-bit digital comparator |
| 4724 | 1 | 8-bit addressable latch |
| 4750 | 1 | Frequency synthesizer |
| 4751 | 1 | Universal divider |
| 4794 | 1 | 8-stage shift-and-store register LED driver |
| 4894 | 1 | 12-stage shift-and-store register LED driver |
| 4938 | 2 | Dual retriggerable precision monostable multivibrator with reset |
| 4952 | 1 | 8-channel analog multiplexer/demultiplexer |